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UTTARAKHAND TECHNICAL UNIVERSITY											
		Pape	r ID and	Roll No	to be fi	illed in y	our Ans	wer boo	k		
Roll	. No.					r	T				
	•		EVEN S	SEMES	TER EX	AMINA	TION-	2019			
MICROPROCESSOR Subject Code: TCS-405											
(Attem	Attempt any four. All question carries equal marks)  4*5 = (20 marks)										
	efine the fo									•	
a)			cessor? Ex	plain in d	etail.						
b)	What is the use of READY signal in 8085?										
c)	Explain P	USH and	POP instr	uctions?							
d)	Explain minimum/maximum mode operation in 8086.										
e)	What is L	XIH, LDA	?								
					Sect	ion B					
(Attem	pt any fo	ır. All qu	estion car	ries equa	l marks)			4*	5 = (20 m	arks)	
1)	What do	you mea	n by supe	rscalar ar	chitecture	?					
2)	Explain memory segmentation in 8086 microprocessors.										
3)	What are	assemb	ler directi	ves?							
4)	Write short note on Dual Core and Core 2 Duo processor?										
5)	What is h	nandshak	ing in Mic	roprocess	sor?						
					Sect	ion C					
(Attempt any four. All question carries equal marks)								4*	4*5 = (20 marks)		
1)	Give the	features	of 80186	& 80286.							
2)	What is i	nstructio	n pipelinir	ng in 8086	5?						
3)	Draw the internal block diagram of 8086 microprocessor.										
4)	What is physical address & offset address. Explain										
5)	Draw the	block di	agram and	d Pin diag	ram of 80	51 Microo	controller.				
					Sect	ion D					
(Attempt any two. All question carries equal marks)								2*	2*10= (20 marks)		
1)	Discuss t	he pin di	agram of	8085 mici	roprocesso	or.					
2)	What are the advance design features of Pentium Processors?										
3)	Write a p	orogram	to divide t	wo 8-bit	numbers.						
					Sect	ion E					
(Atten	npt any tw	o. All qu	estion car	ries equa	l marks)			2*	'10= (20 n	narks)	
1)	Discuss i	nterfacin	g of matri	x keyboa	rd with 80	51 with n	eat and cl	ean diagr	am.		

2) Explain in detail the 80486 system with block diagram.

3) Explain programmable peripheral interface (PPI).